

FIG. 1

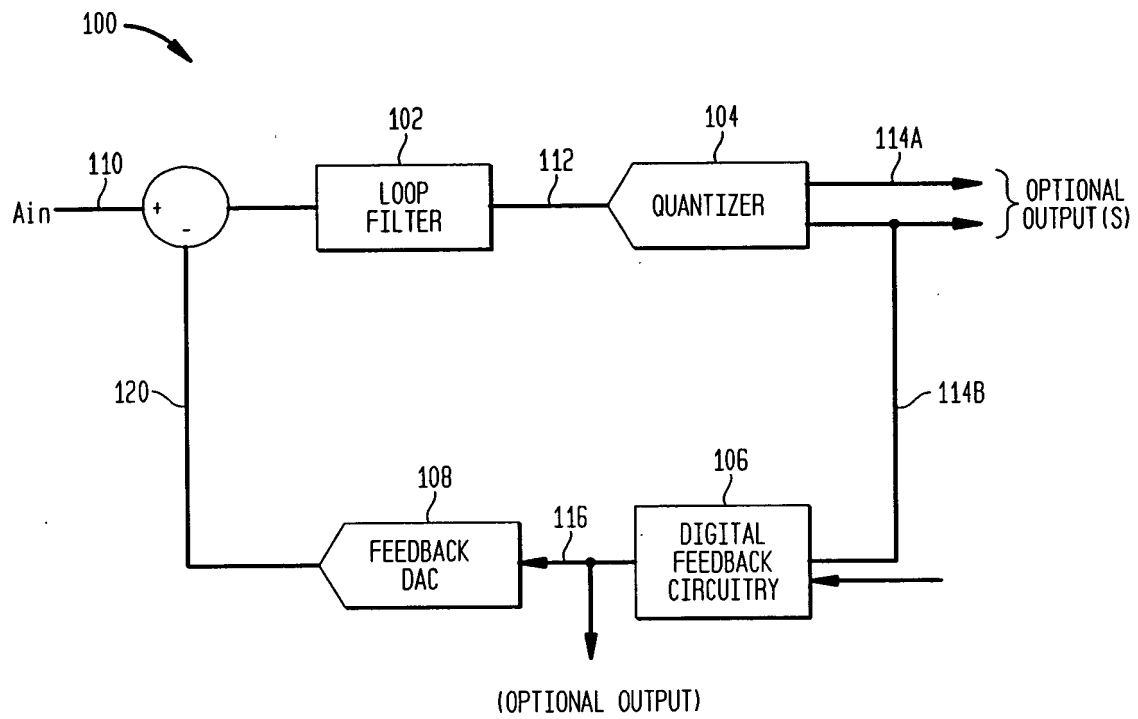


FIG. 2

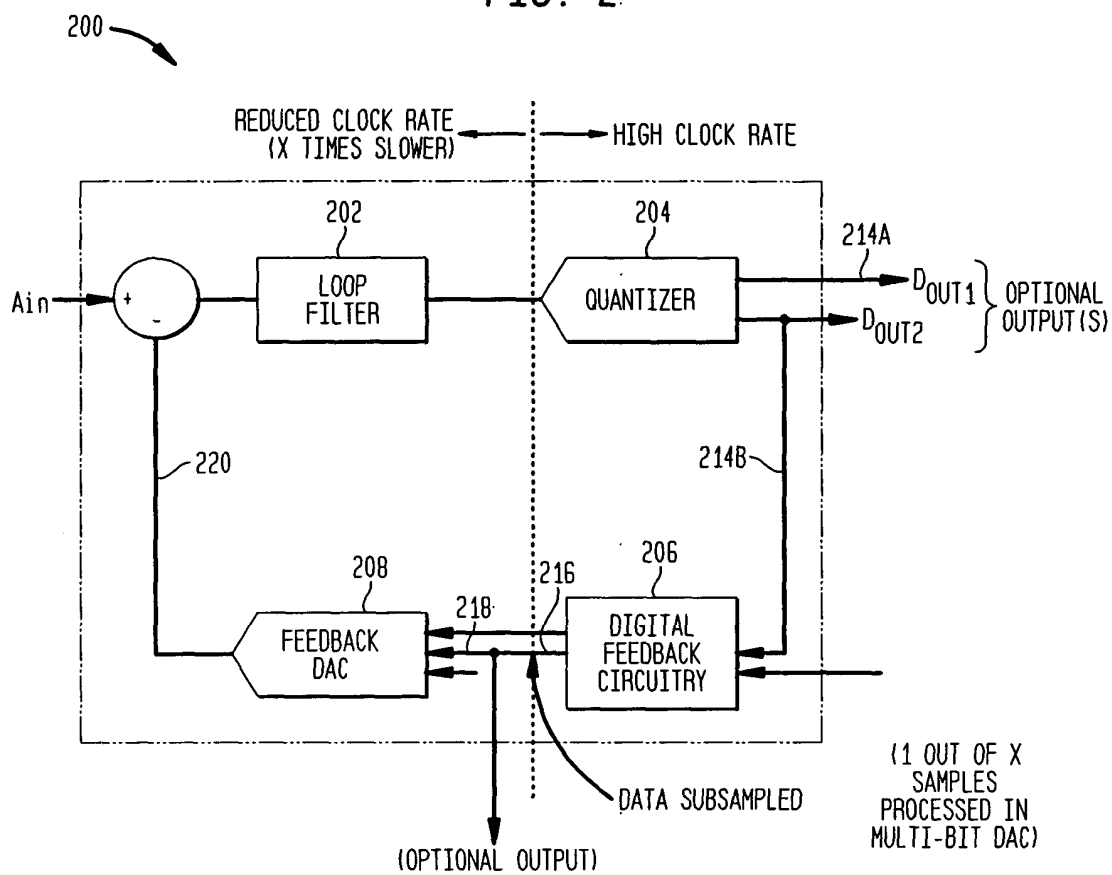


FIG. 3

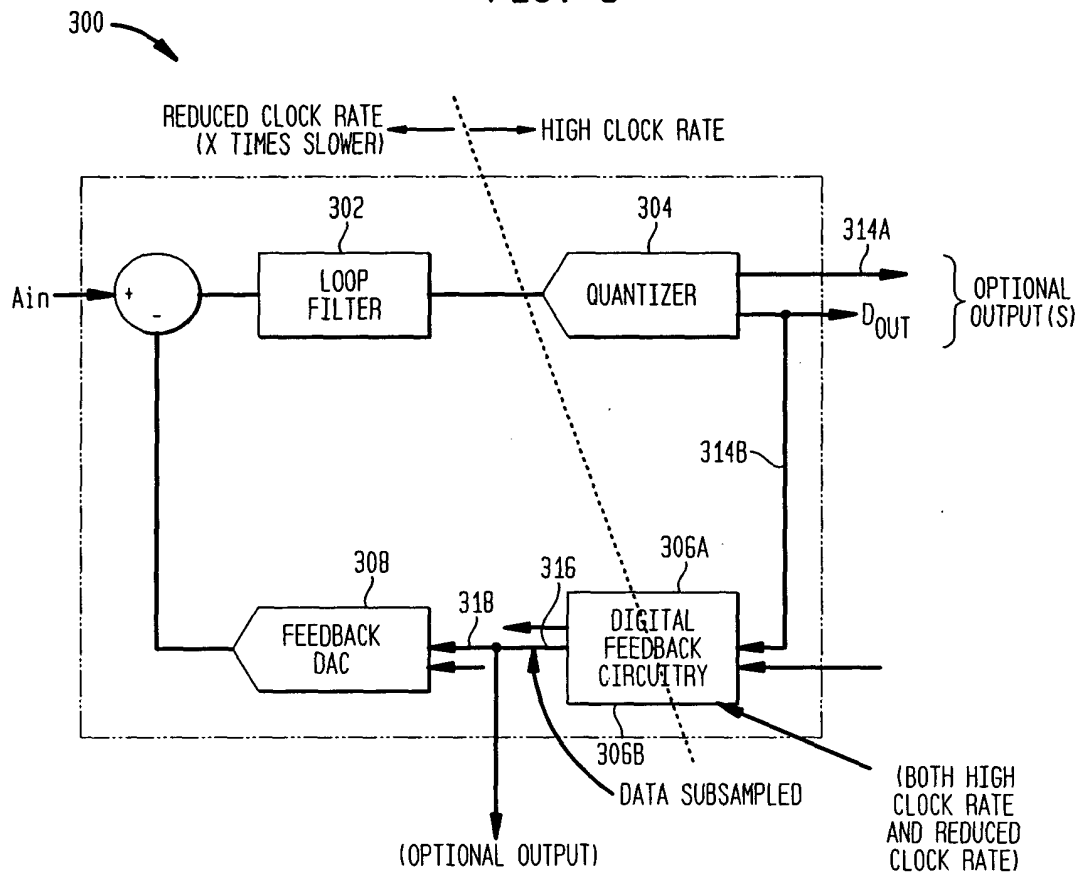
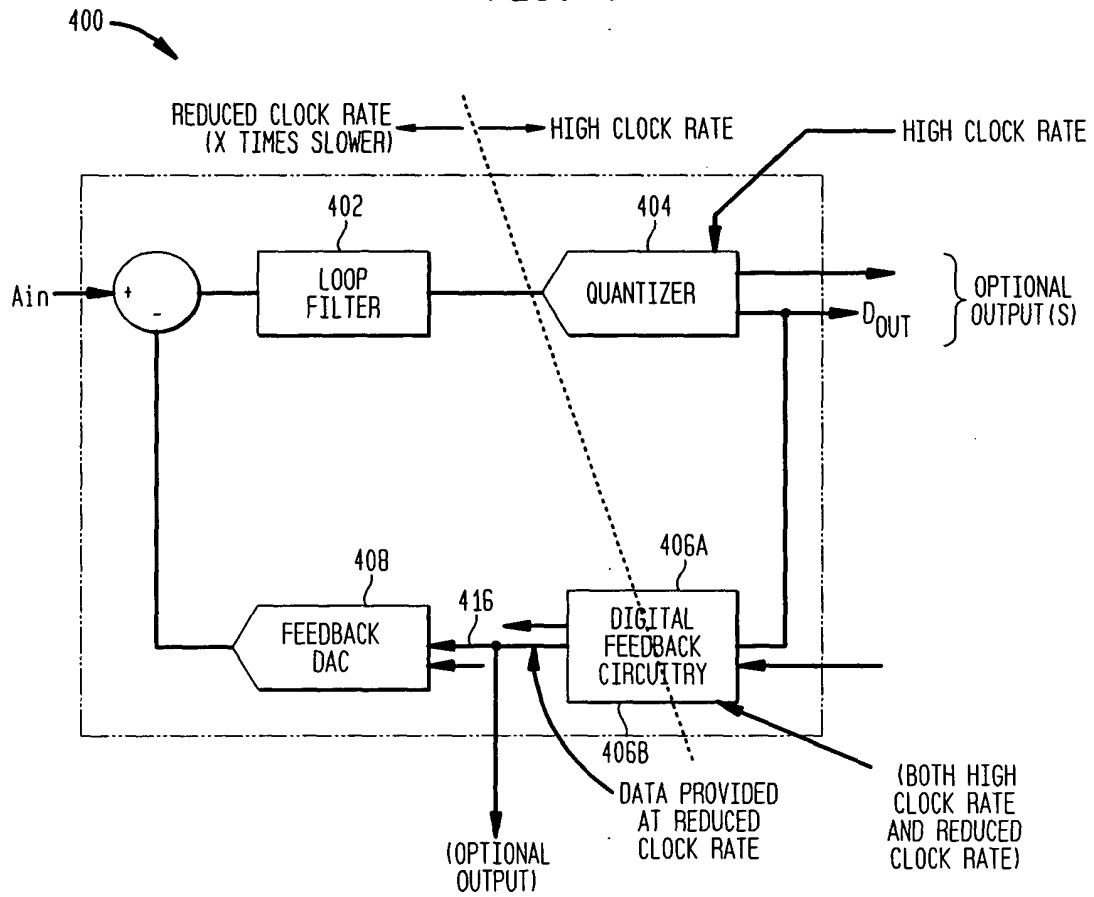


FIG. 4



**FIG. 5**

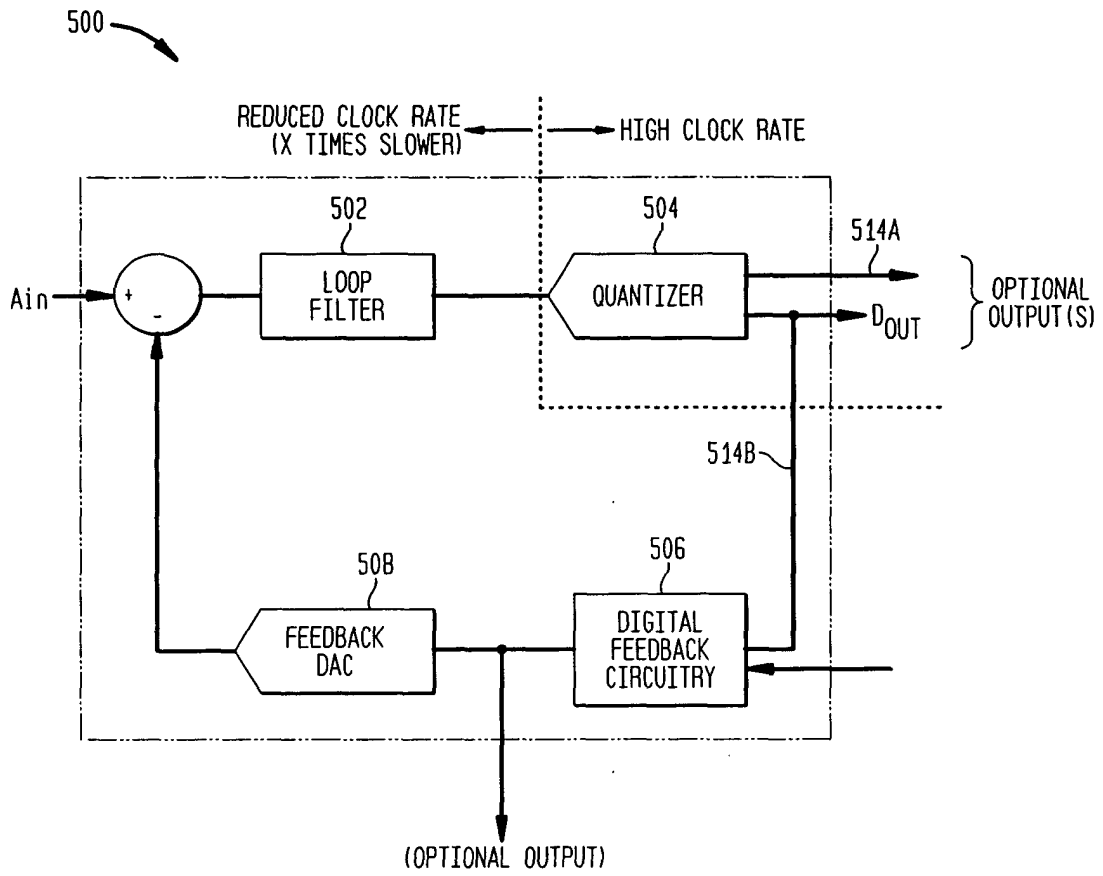


FIG. 6

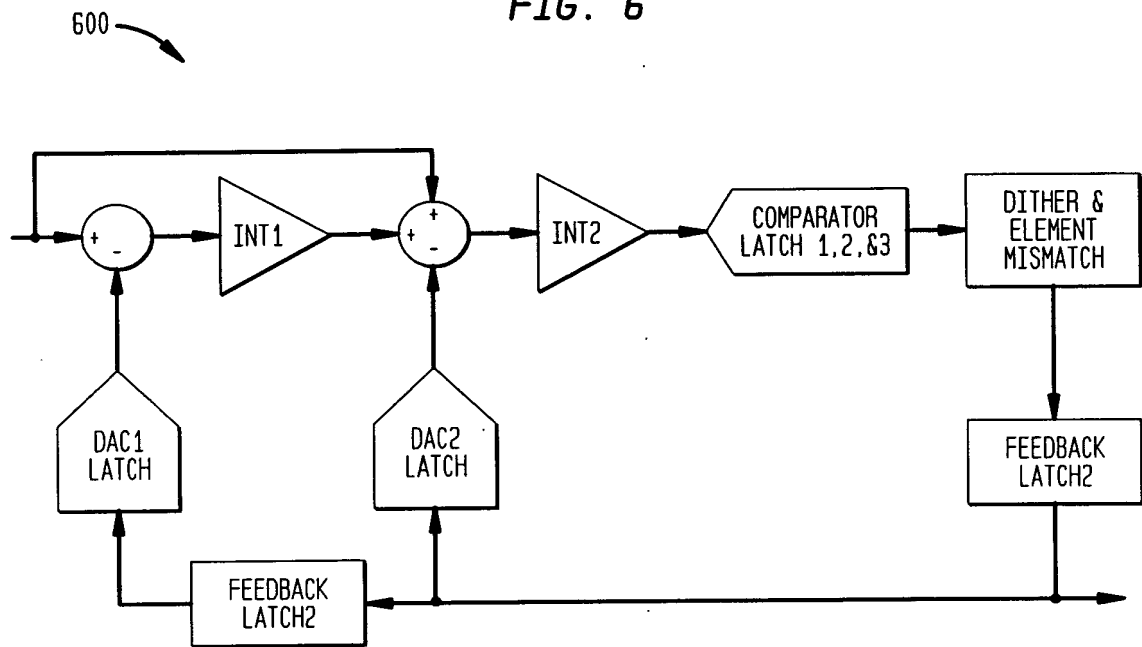




FIG. 7B

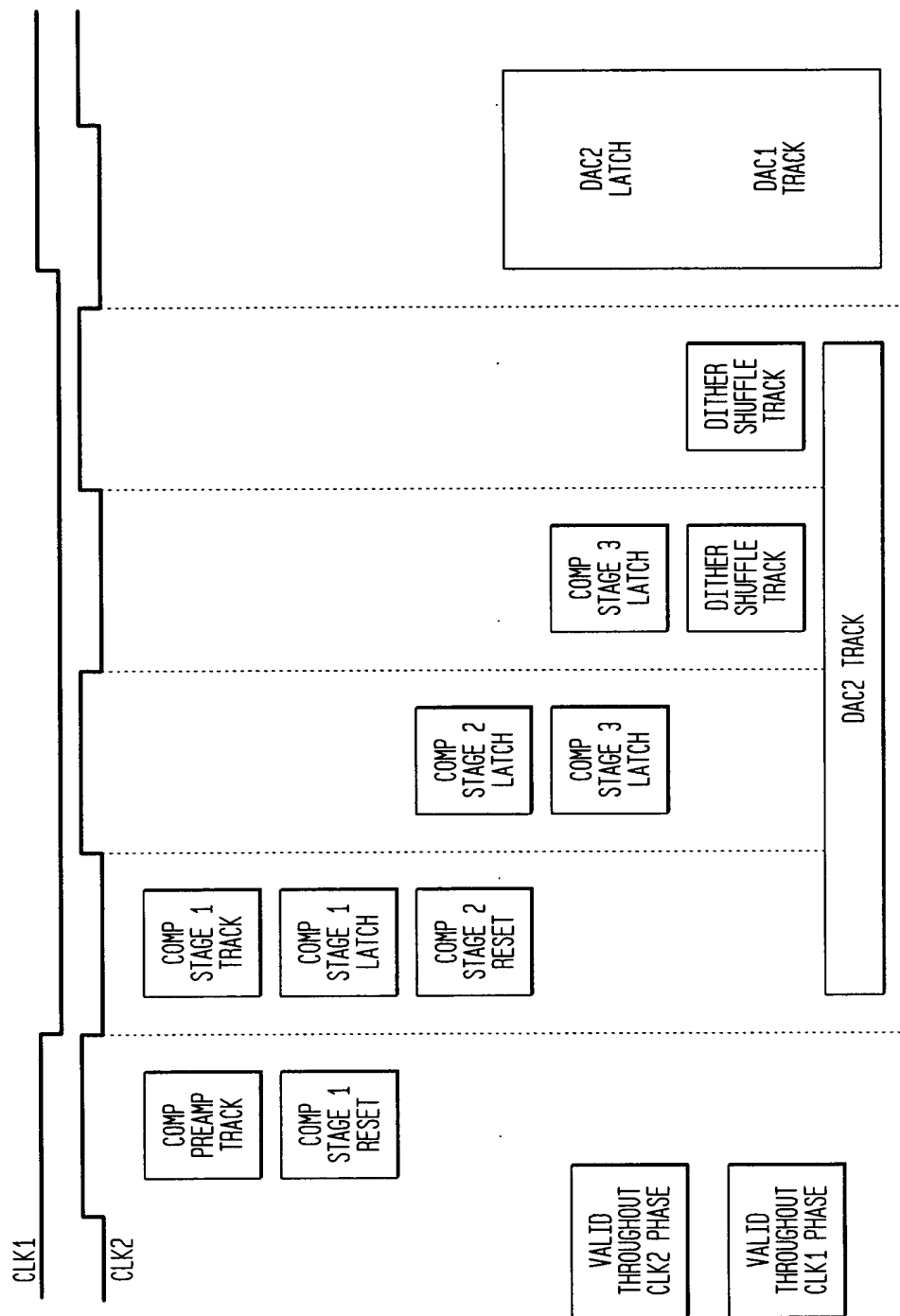




FIG. 7C

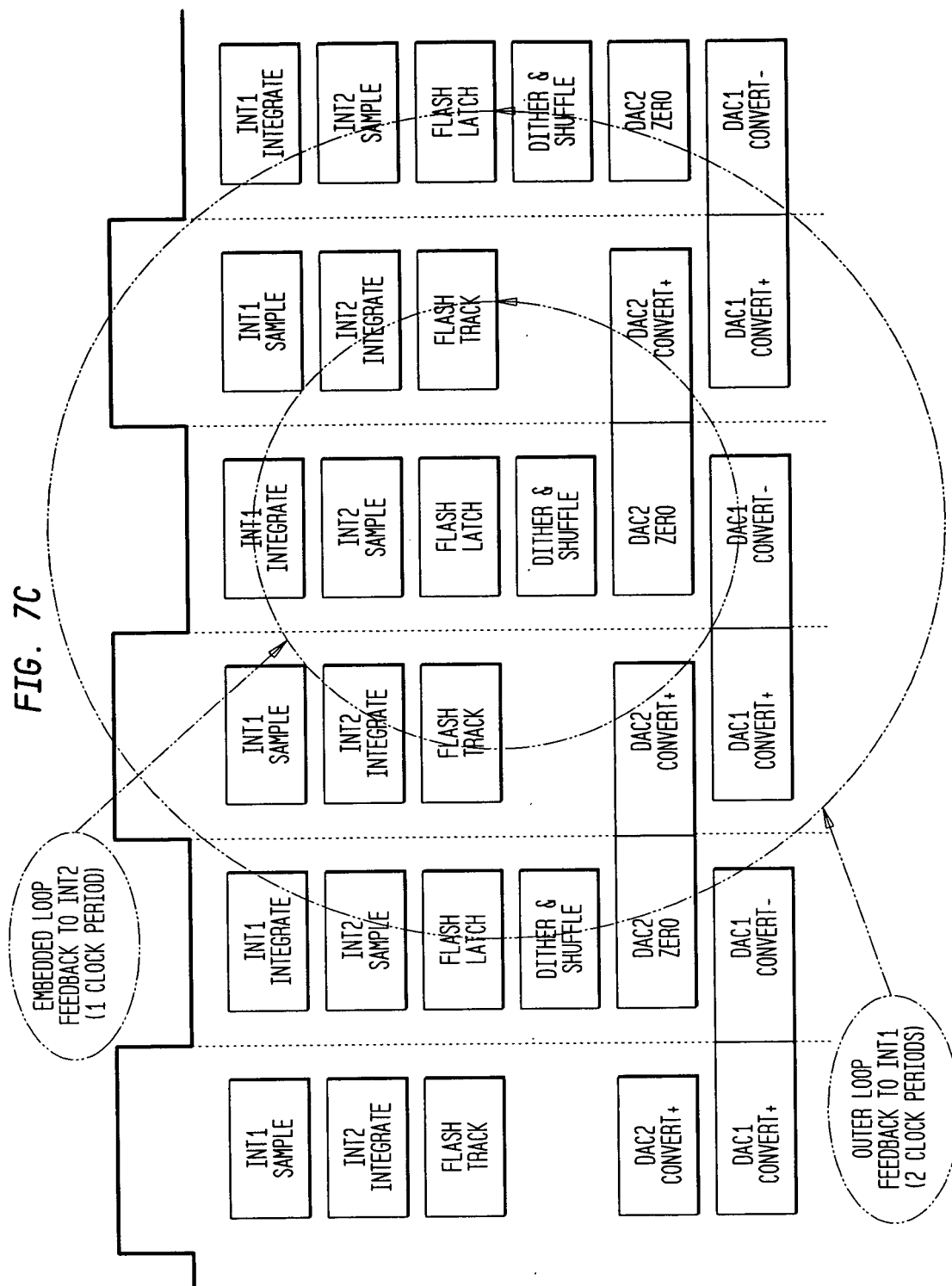


FIG. 8

800

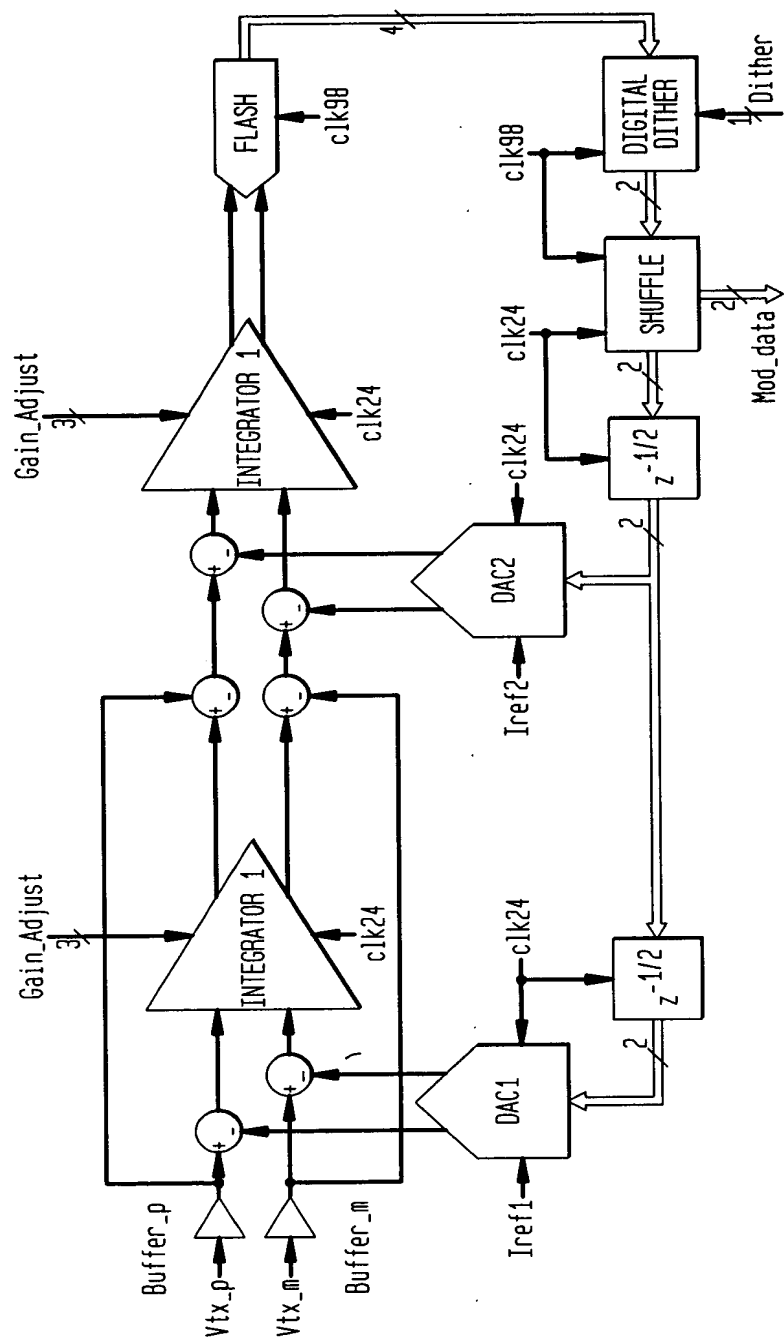
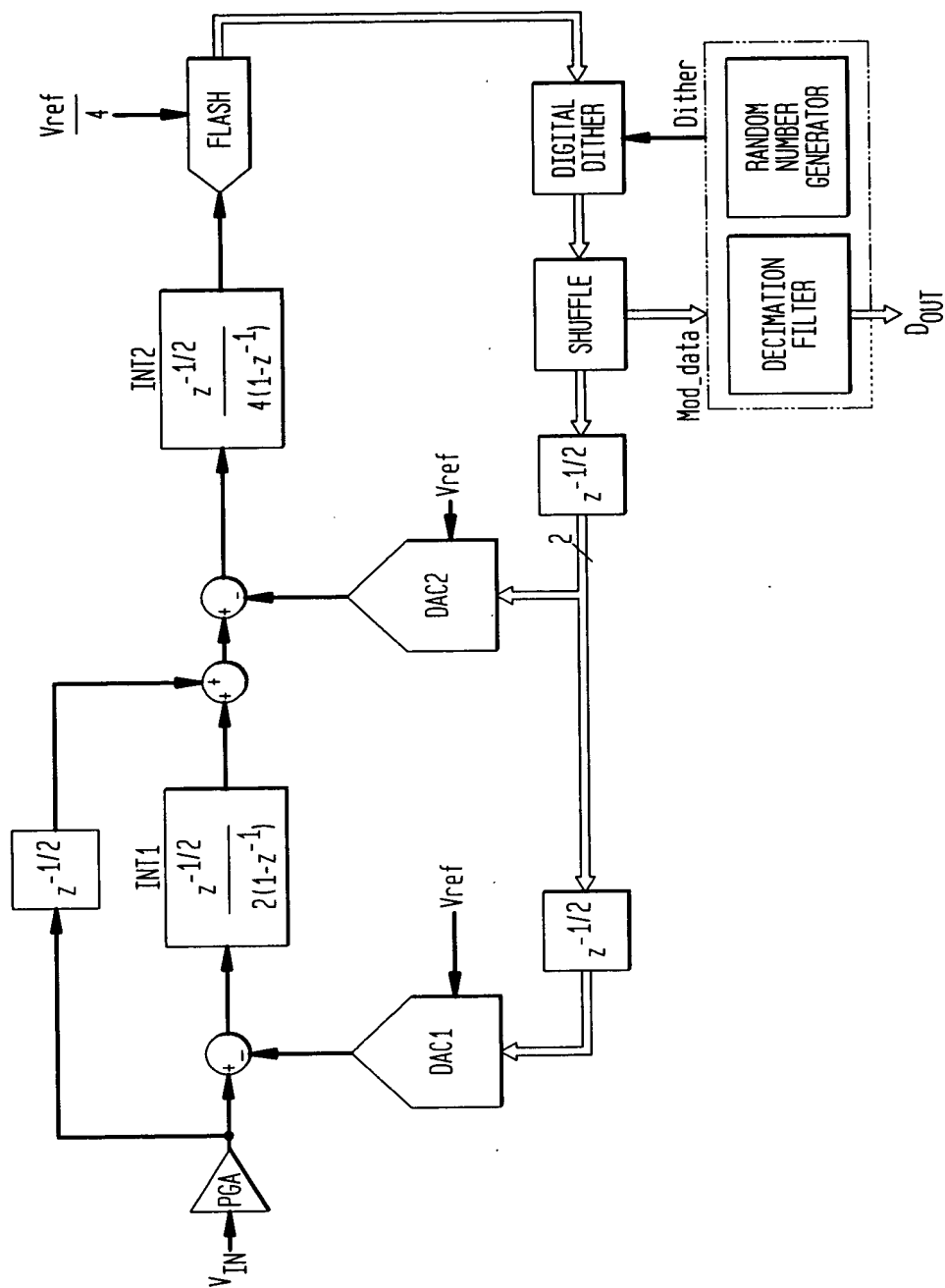
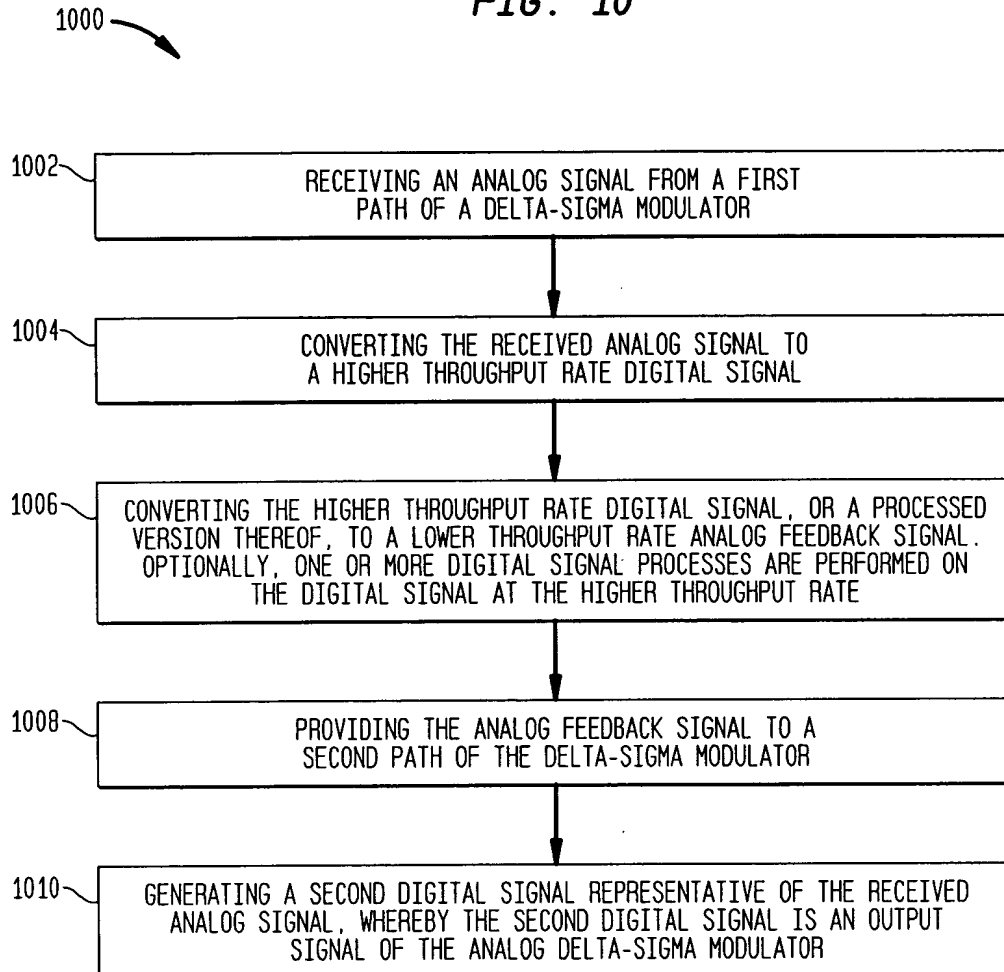


FIG. 9



**FIG. 10**



**FIG. 11**

1100

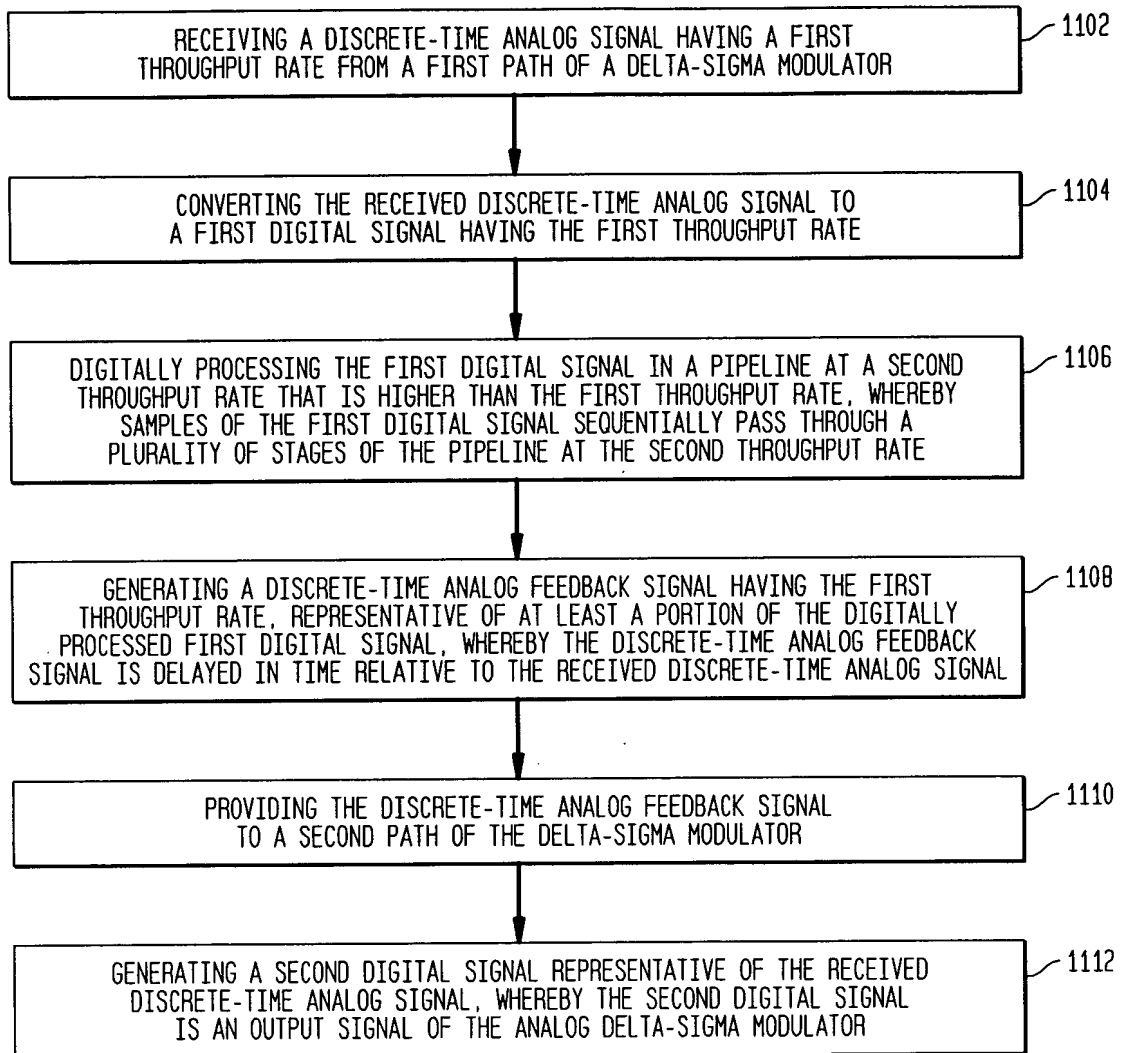
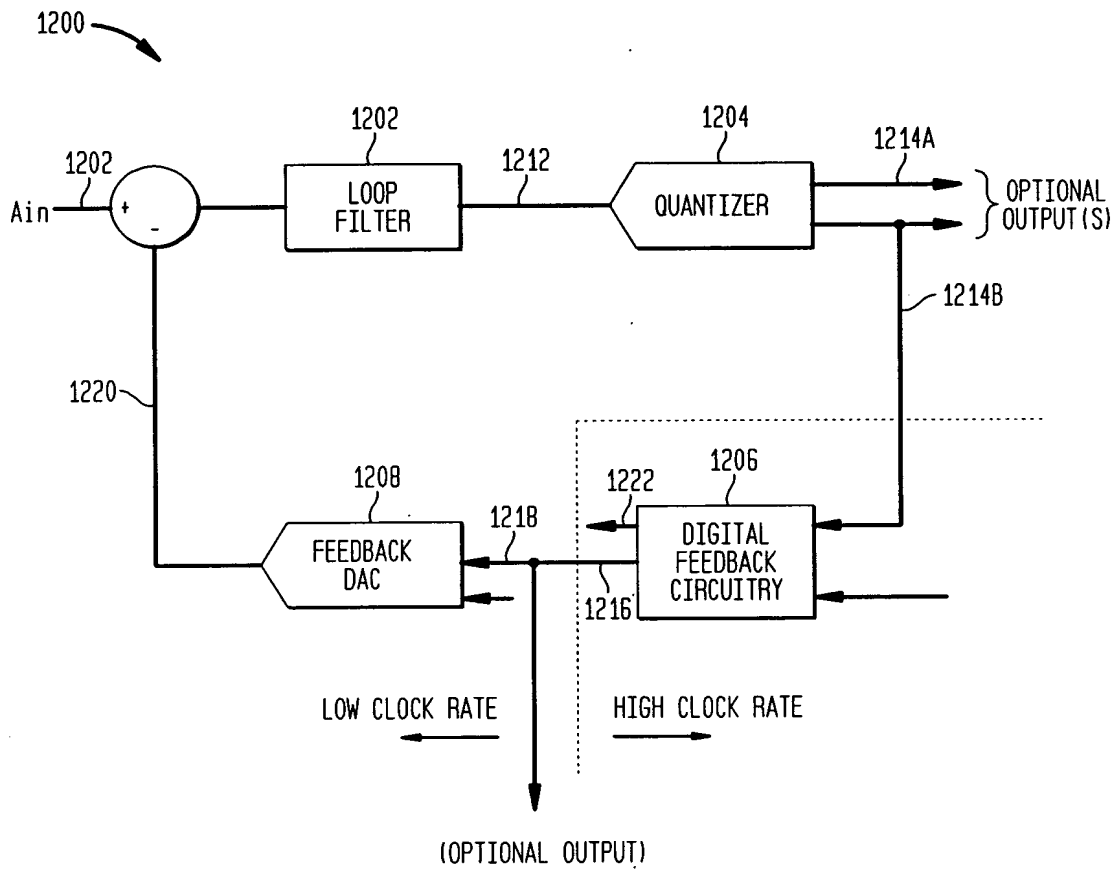
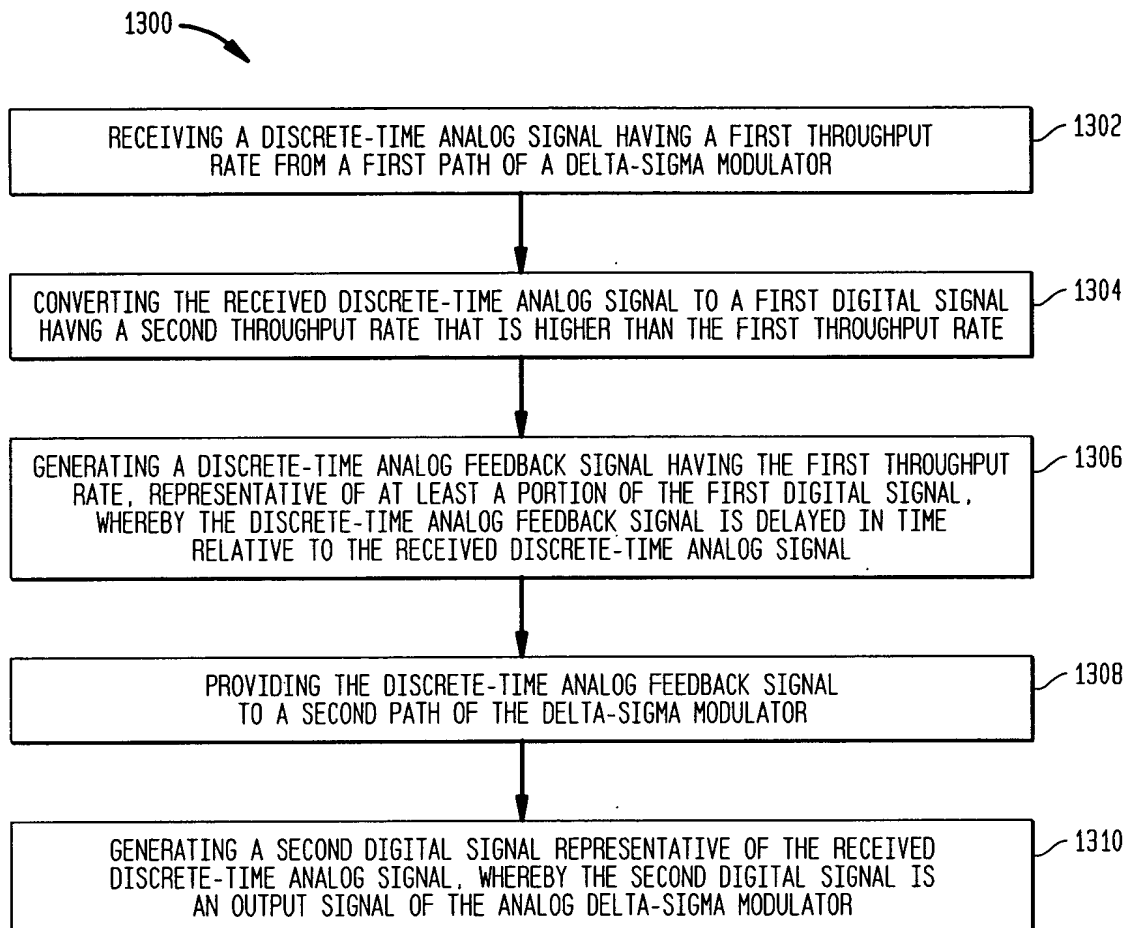


FIG. 12



**FIG. 13**



1400

**FIG. 14**

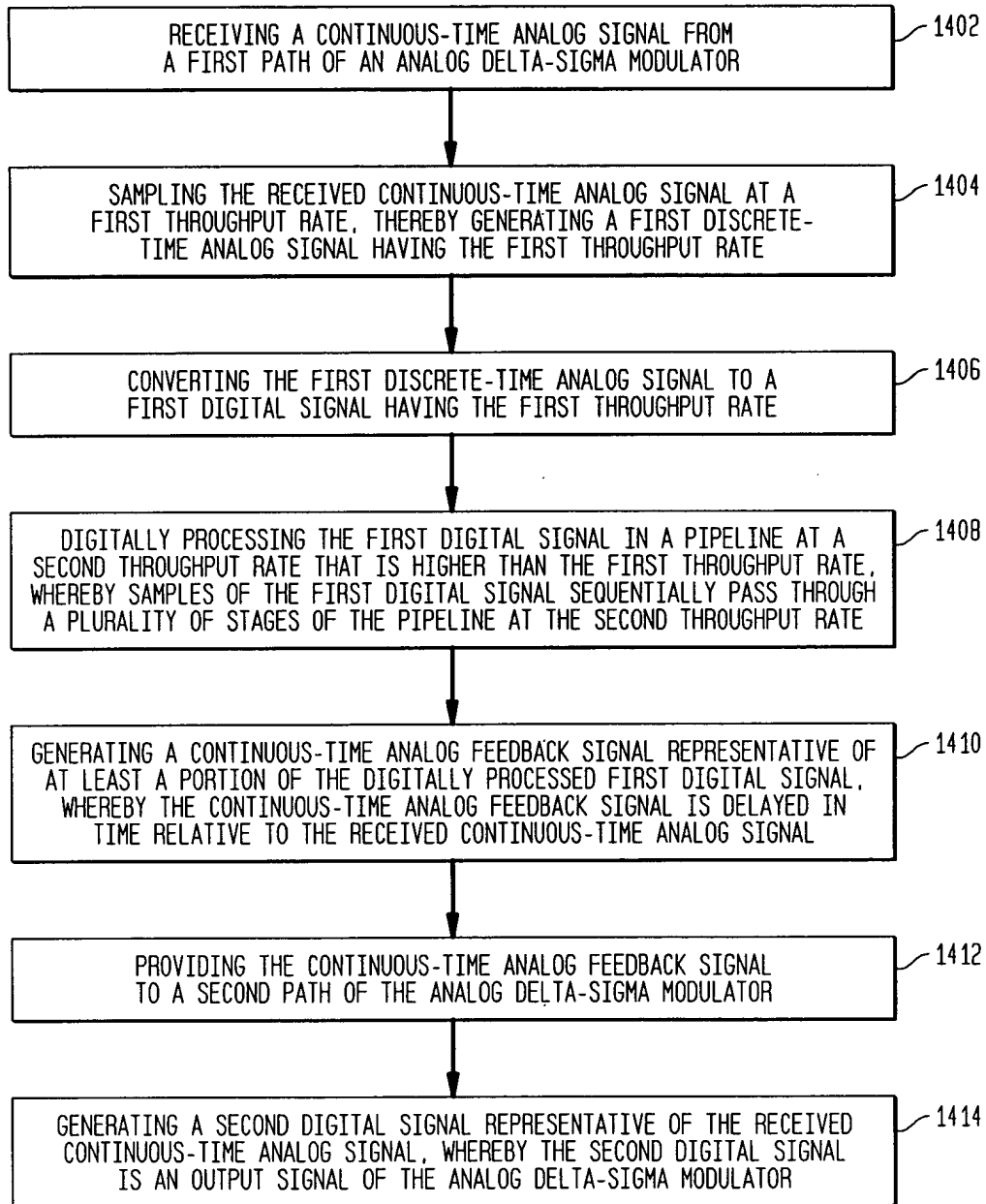




FIG. 15A

TC4011 A/D MODULATOR TIMING

▨ = INVALID DATA

▨ = SAMPLE "n" DATA

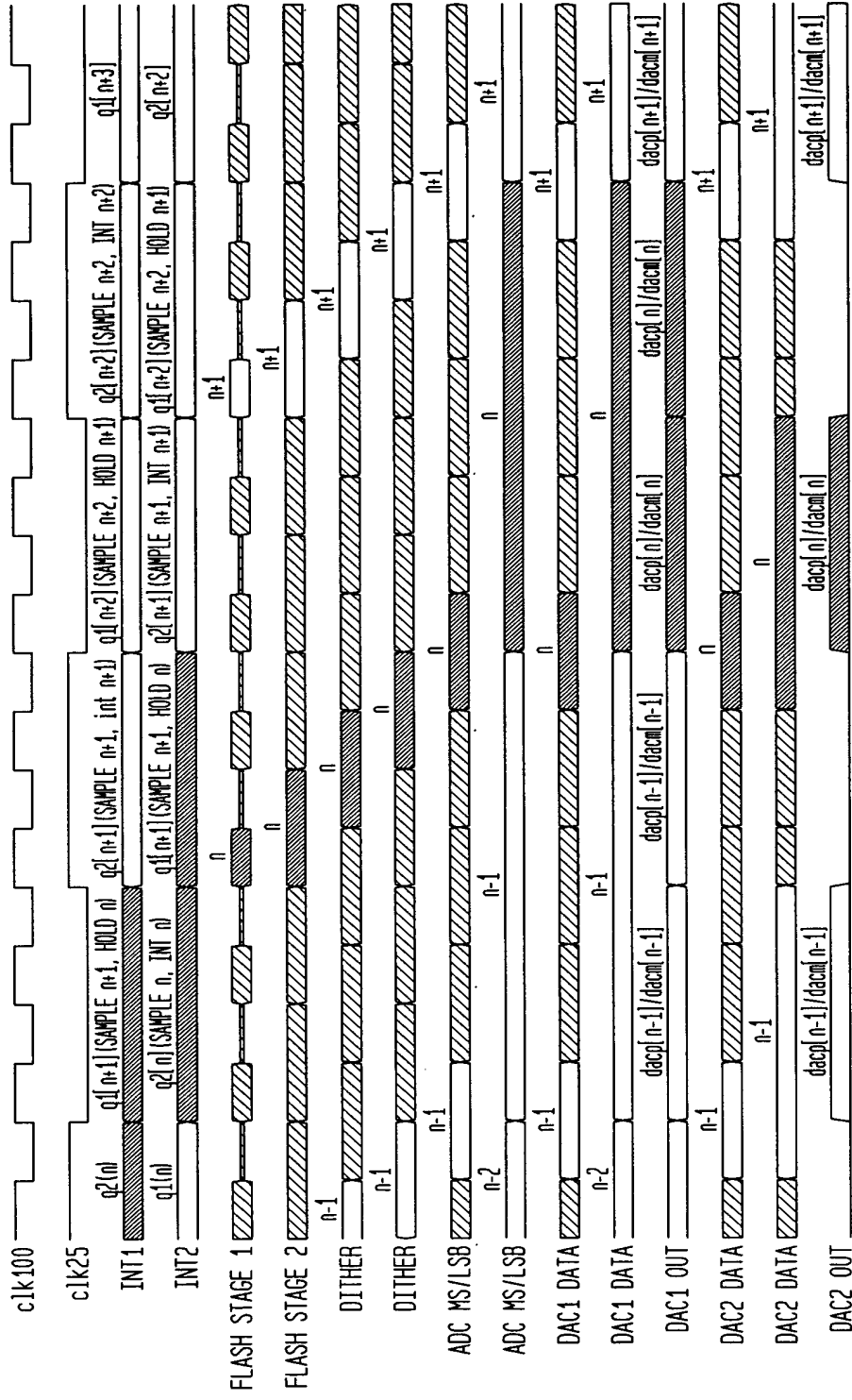


FIG. 15B

BCM3352 A/D MODULATOR TIMING

▨ = INVALID DATA

▩ = SAMPLE "n"s DATA

